

FIG 1A.

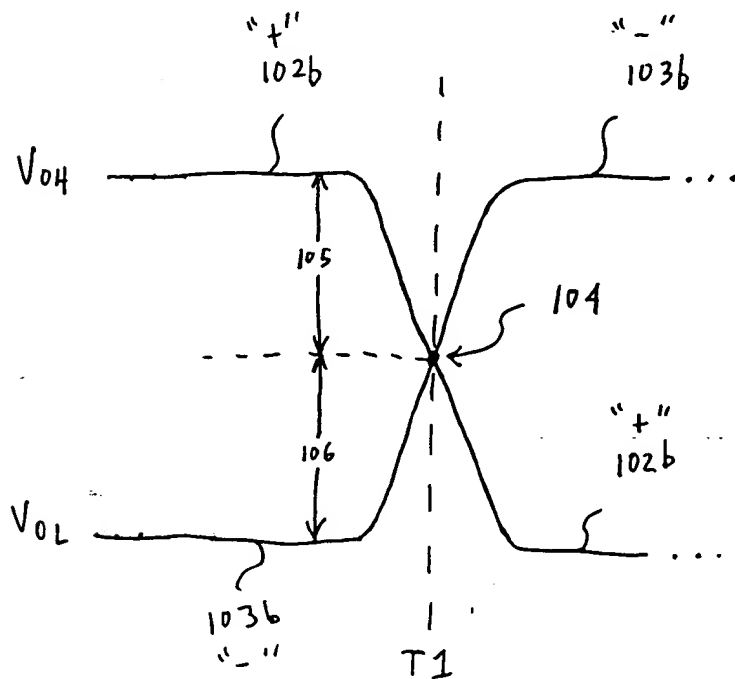
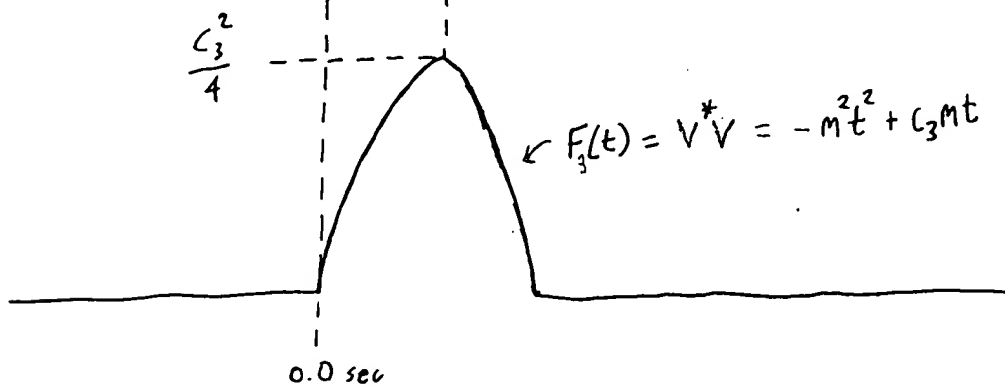
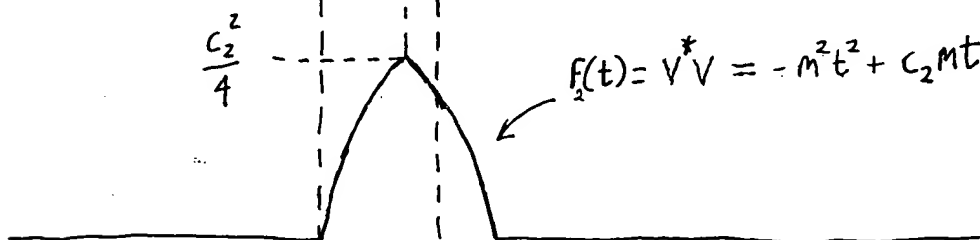
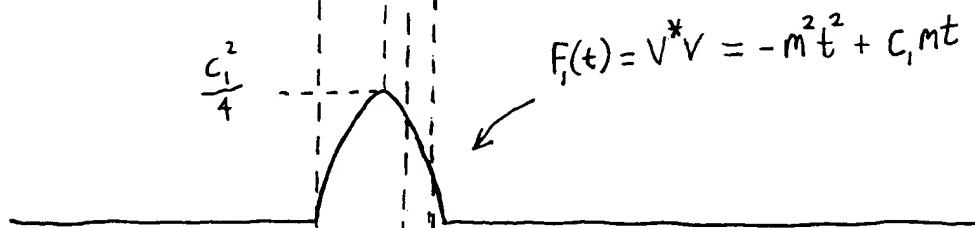
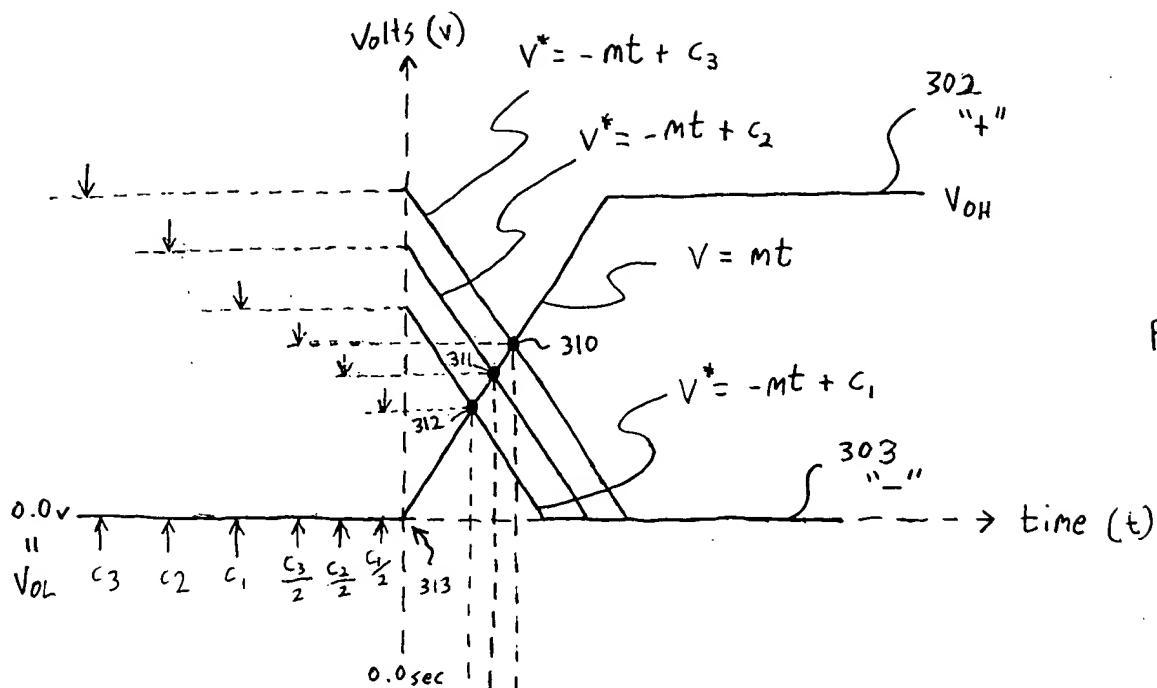


FIG 1B.

FIG 1A. FIG 1B.

A cross-sectional view of a semiconductor device. The device features a central region 204a, 204b, and 204c, which are interconnected by a vertical dashed line. This central region is surrounded by a layer 203a, which is further surrounded by a layer 202a. The device is connected to a high voltage terminal V_{OH} and a low voltage terminal V_{OL} . The central region is also connected to a terminal 203c, which is further connected to a terminal 202c.

09067150-092704
FD260-6979660

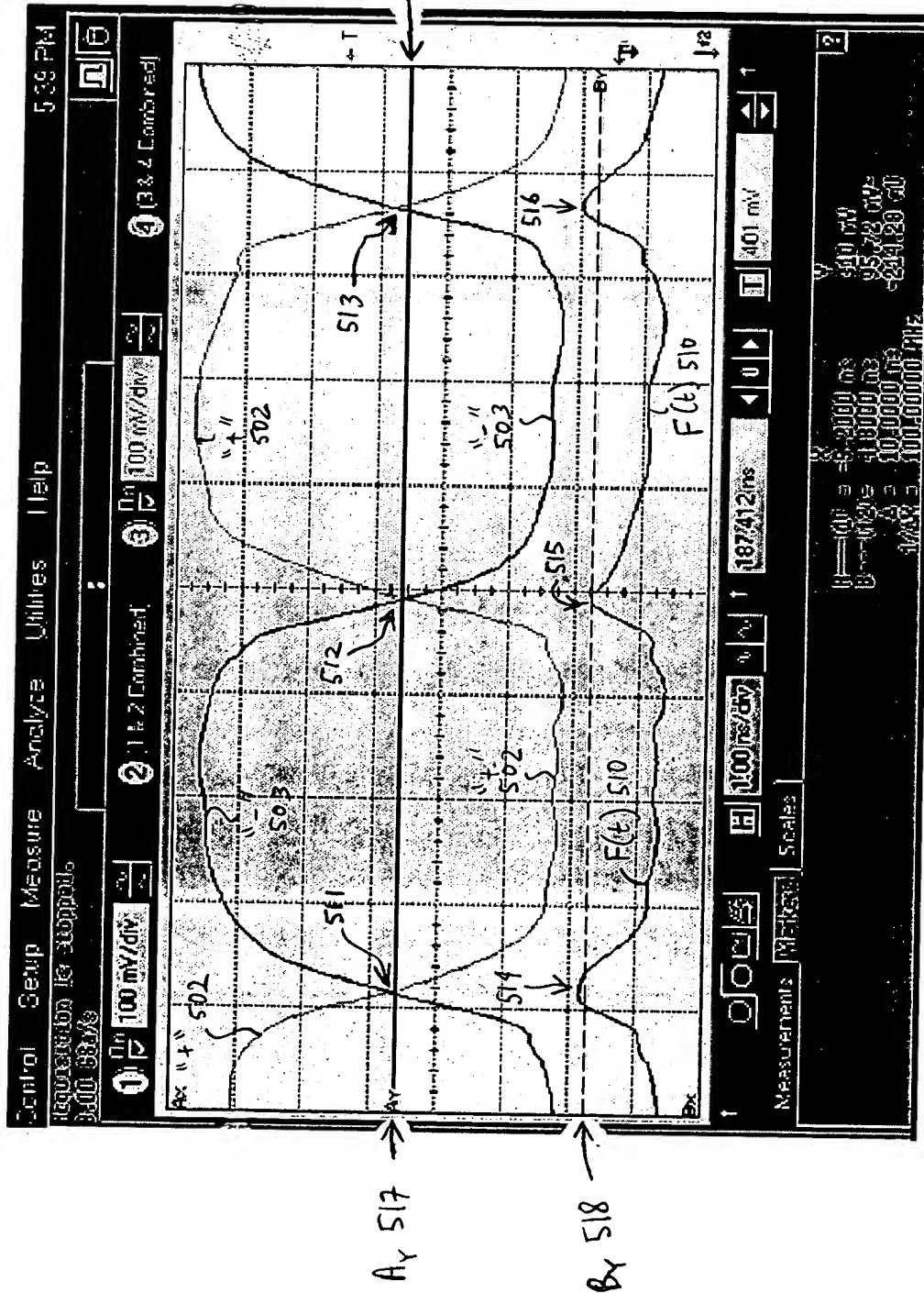


```

graph TD
    401[SAMPLE "+" & "-"  
WAVEFORMS OF A  
DIFFERENTIAL SIGNAL  
OVER A LOGICAL TRANSITION] --> 402[DETERMINE THE PRODUCT  
OF THE "+" & "-"  
WAVEFORMS OVER THE  
LOGICAL TRANSITION]
    402 --> 403[DETERMINE THE SQUARE ROOT  
OF THE PRODUCT MAXIMUM TO  
DETERMINE THE CROSSING  
POINT WITHIN THE LOGICAL  
TRANSITION]
  
```

FIGURE 4.

TD2260-65729660



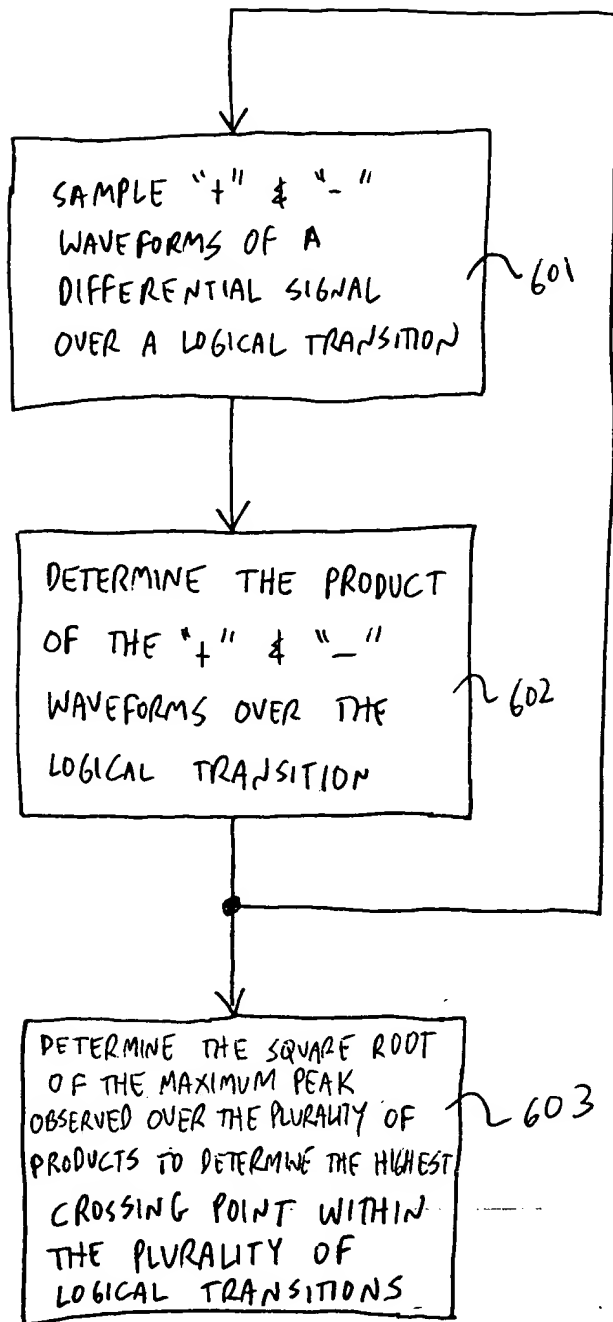
[illegible]

FIGURE 6

FDZ260-69T29660

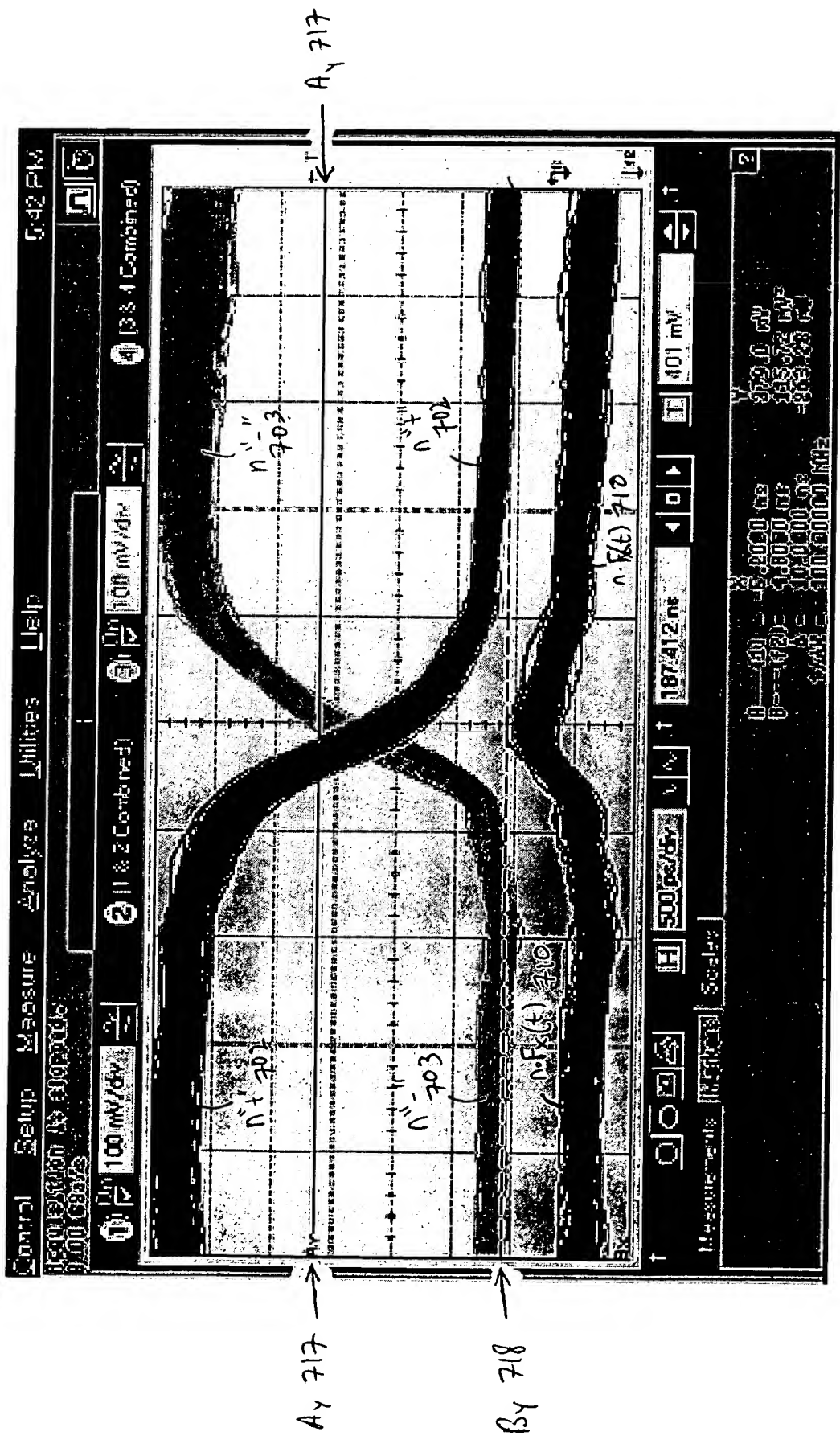


FIGURE 7

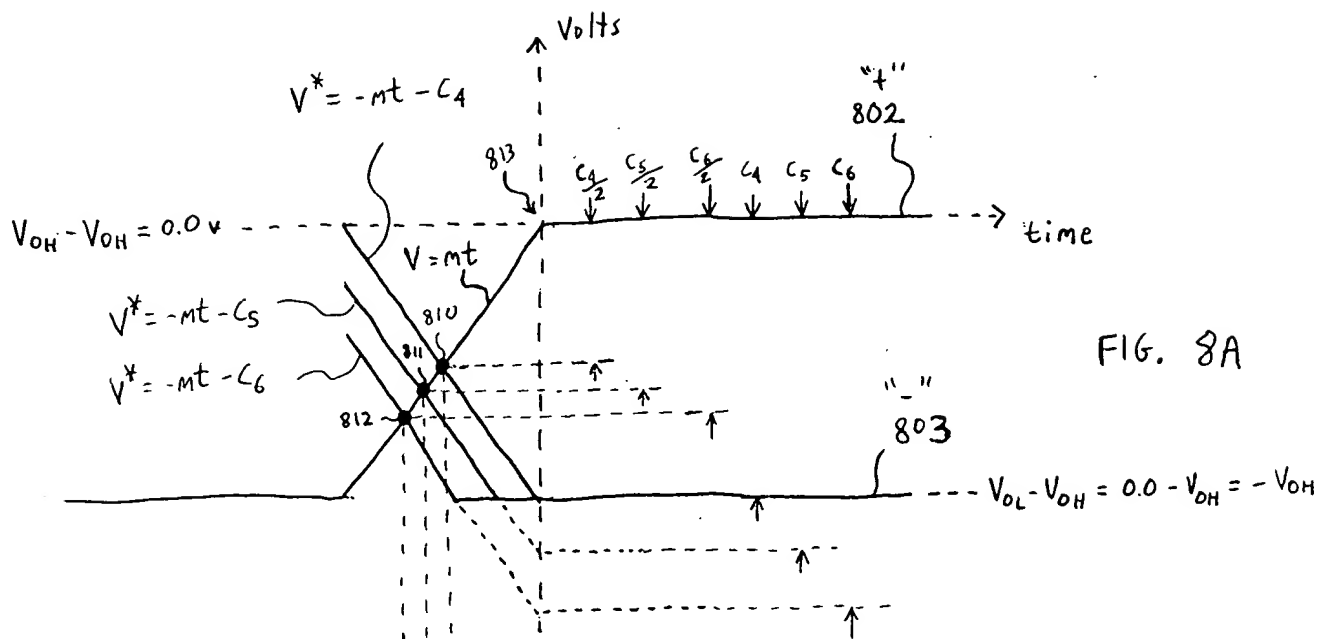


FIG. 8A

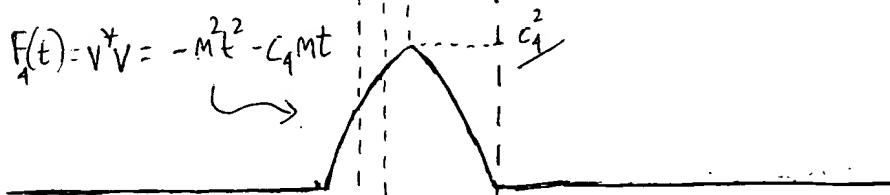


FIG. 8B

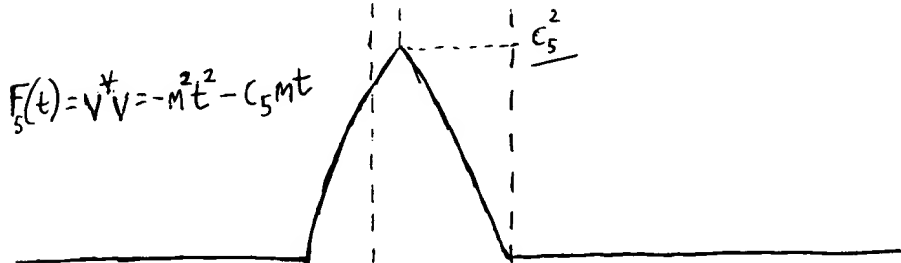


FIG. 8C

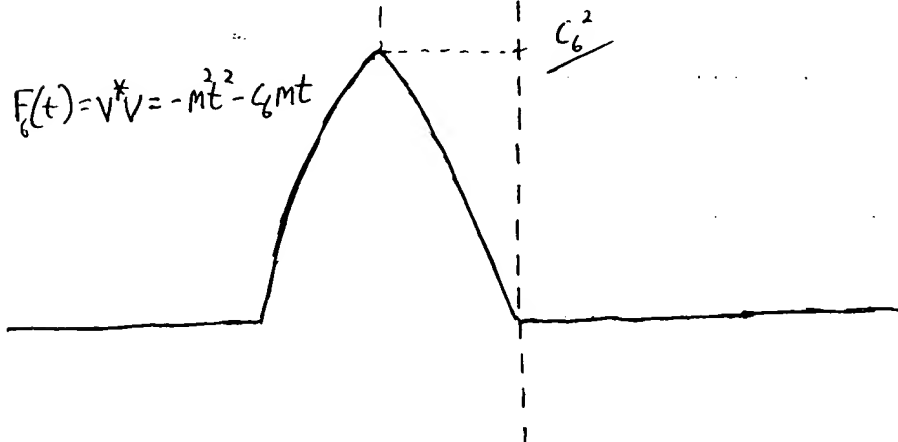


FIG. 8D

Figure 1

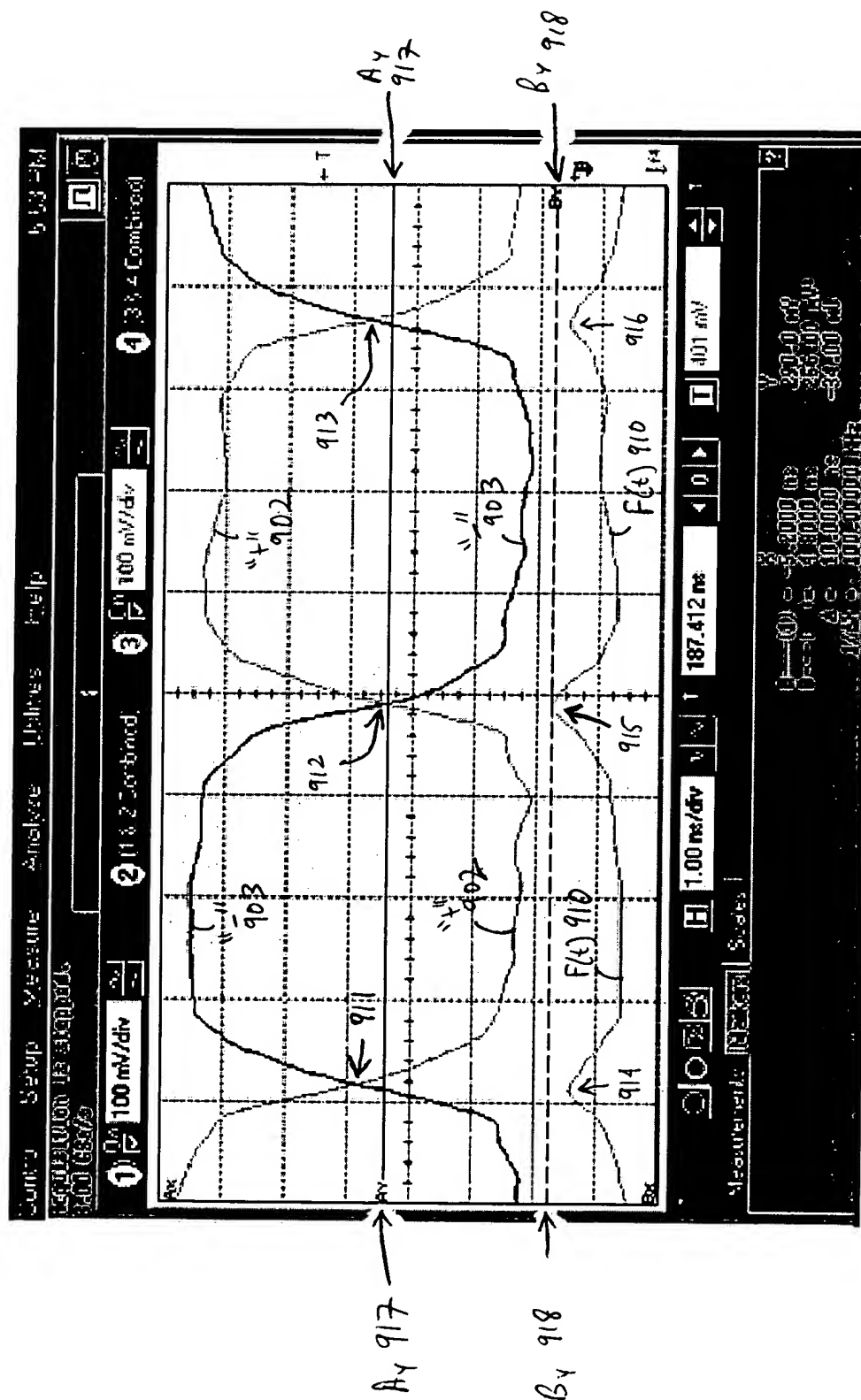


FIGURE 9

FO2260-69729660

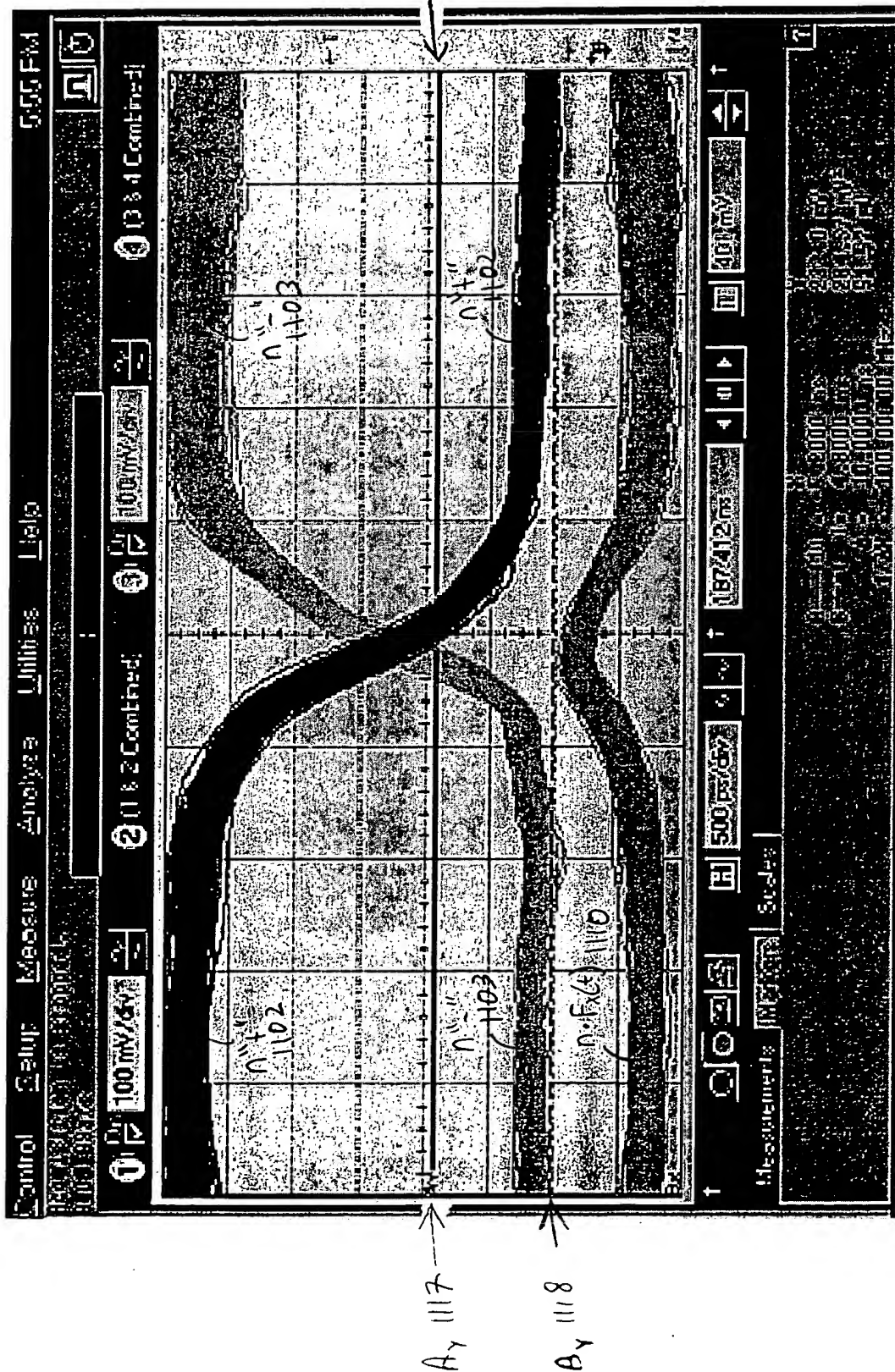


FIGURE 11